

# Recommended Practice for Hardware-in-the-Loop (HIL) Simulation Based Testing of Electric Power Apparatus and Controls – IEEE WG P2004

Mischa Steurer, Chair

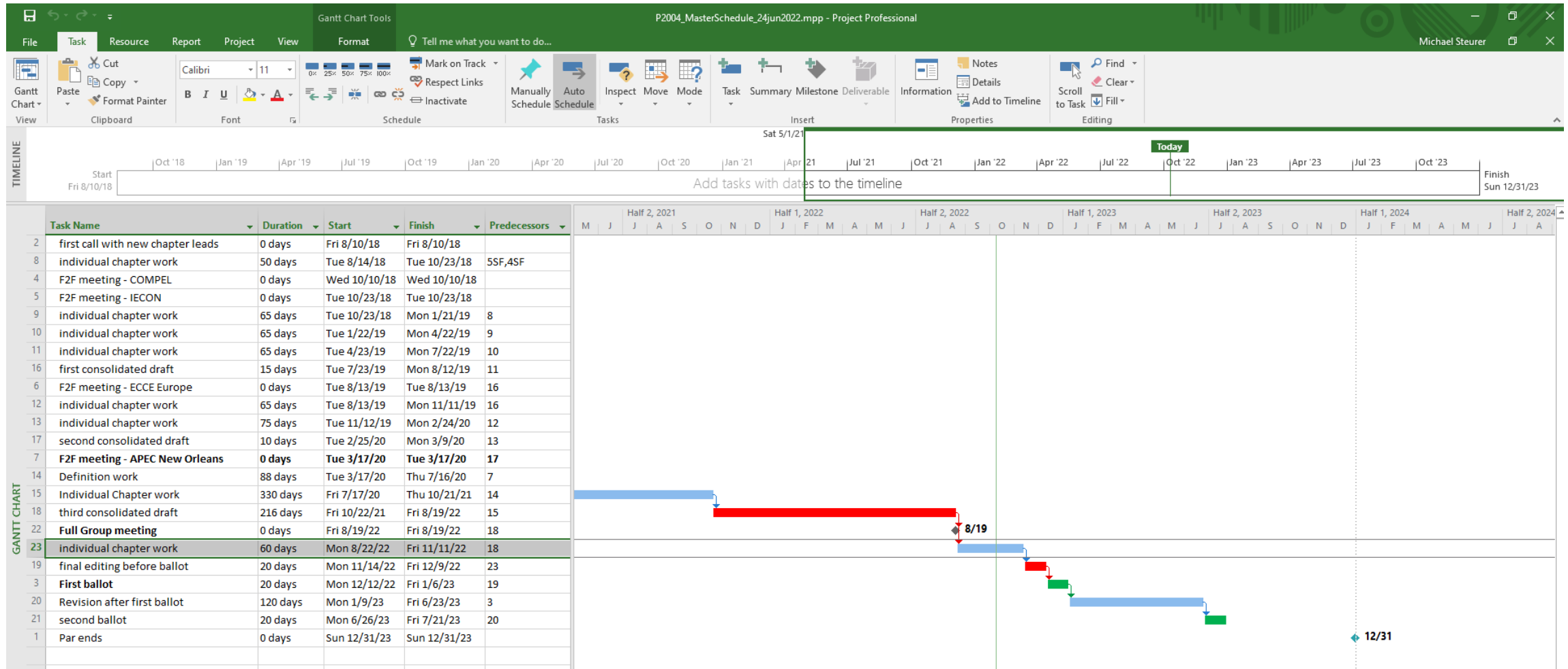


# Updates

- 3<sup>rd</sup> consolidated draft:
  - The editorial board worked for several months to create a major revision to ensure only normative language in main body
  - Delivered D3 back to the chapter leads (and hence the full group) for review and edit of appetencies
- Final editing before balloting planned for the end of CY22
- Expect to ballot early 2023

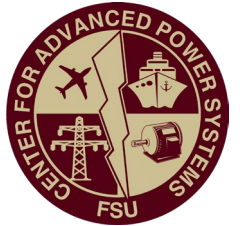
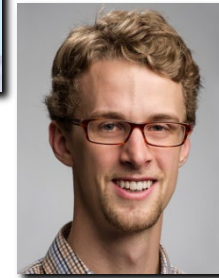
# Overview of Progress on P2004 Development

- Timeline (status 10/10/2022)



# IEEE WG P2004 Structure

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- Sponsor: **PELS**, Co-sponsor: **IAS, IES**
- PAR ends **12/31/2023**
- Data repository<sup>(1)</sup>: <https://iee-SA.imeetcentral.com/p2004/>
- Collaboration: **PSRC WG CTF-33**; IEEE task force (TF) on “Real-Time Simulation of Power and Energy Systems”, chaired by Dr. Omar Faruque, under **IEEE WG 15.08.09** (within the General System Subcommittee of the IEEE PES T&D Committee)



<sup>(1)</sup> Request access via email

# IEEE P2004 Chapter Leads

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