A Silicon Ingot Lifetime Tester for Industrial Use

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Presented at the National Center for Photovoltaics Program Review Meeting Denver, Colorado September 8-11, 1998



National Renewable Energy Laboratory 1617 Cole Boulevard Golden, Colorado 80401-3393 A national laboratory of the U.S. Department of Energy Managed by Midwest Research Institute for the U.S. Department of Energy under contract No. DE-AC36-83CH10093

Work performed under task number PV902401

October 1998

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Abstract. A specially designed lifetime measurement instrument has been developed to characterize silicon ingots before they are subjected to expensive slicing and solar-cell processing, thereby saving needless processing costs of inferior materials in a solar-cell production line. The instrument uses the direct-current photoconductance decay (DC-PCD) method for linear detection of the transient photoconductance signal and localized probing / illumination for necessary sensitivity on low resistivity and large samples. The instrument also has a compact and high-power laser diode as the light source, data averaging capability, a pneumatic ingot transport and probe positioning mechanism, and a user-friendly graphical interface for data acquisition / lifetime calculation / data storage / hardcopy for factory-floor use with quick turnaround. A 3-dimensional finite-element analysis indicates that the as-cut surface finish is adequate for measuring the bulk lifetime on the order of 50 μ s or less. Measurement repeatability and clear distinction among different grades of feedstock materials have been demonstrated.

INTRODUCTION

In commercial production of crystalline-silicon solar cells, a variety of feedstock materials may be used due to cost and availability considerations. As a result, the Czochralski-silicon (CZ-Si) crystals could have varying impurity levels and defects that may render the crystals too inferior to yield decent cell efficiency. Lifetime characterization of the crystal ingots before slicing could save the unnecessary processing costs of these materials.

The direct-current photoconductance decay (DC-PCD) method is the standard technique that can give accurate lifetime values if certain conditions are met per ASTM standard [1], but it requires destructive and laborious sample preparation with electrical contacts and it is not applicable on large ingots directly. However, the commonly used contactless techniques, e.g., microwave reflection or radio-frequency photoconductance decay (μ -wave-PCD or RF-PCD), have a carrier-density-dependent skin effect that emphasizes surface recombination and non-linear circuit response to photoconductance and generally require a tuning operation for each measurement. Hence, a non-destructive, inexpensive, preparation-free, and easy-to-use instrument needs to be developed to measure bulk minority-carrier lifetimes on an as-cropped surface of a low-resistivity and large ingot.

LIFETIME TESTER

We chose the DC-PCD method for circuit linearity and simplicity for the ingot lifetime tester. To eliminate the need for contact preparations and to generate a sufficient photoconductance decay signal for low-resistivity and large ingots, a pair of probes with ohmic-contact behavior with silicon is used. A long-pulsed laser beam (~250-µs width, <100-ns cut-off) is delivered onto the as-cropped silicon surface between the probes by a high-power (60-Wp) 940-nm laser diode array. A block diagram in Fig. 1 illustrates the system configuration.



Fig. 1 Block diagram of the Lifetime Tester. Half of the silicon ingot sample is shown with the finite-element grid used for PCD simulation.

The photo-induced conductance transient is measured by the oscilloscope that averages, stores, and transmits the data to the PC running a graphical data acquisition program in LabVIEW[®]. A least-squares fit to the data curve and an effective lifetime are calculated within a chosen time period. The program can then store the data for retrieval to refit at a different time interval and print out a hardcopy. The choice of the time interval for lifetime calculation and the relevance of the effective lifetime to the bulk

lifetime will be discussed in the following section. The system has a pneumatic stage for positioning and bringing the ingot into contact with the probes. The load spring in the probe assembly, a proximity switch, and a limit switch determine the probe pressure, which does not affect a given measurement if the pressure is maintained during the signal transient. Fig. 2 is a picture of the instrument.



Fig. 2 The Silicon Ingot Lifetime Tester.

ANALYSIS

Using the probe setup, we need to find out how the electric potential and current are distributed in the sample and how much of an effect the surface has on the PCD signal. A 3-dimensional finite-element analysis is performed for this purpose (For 2-D modeling, see [2]). To minimize computation, a small sample geometry (radius = 1.0 cm, height = 1.0 cm) was chosen, but a much larger ingot does not change the distribution substantially for the same probe spacing. Figs. 3a and 3b show the potential contours on the top surface and the center vertical plane, respectively.



Fig. 3a Electric-potential contours on the top surface.

It is noted that even when a total voltage of 10 volts was applied between the two probes, only about 1 volt is actually dropped in the center excitation region of about 3-mm width. A large voltage drop occurs around each probe tip, which makes the PCD signal sensitive to mechanic-stability of the contacts.

The current density in the center cross-section between the two probes, shown in Fig. 4, points out that the current is somewhat concentrated near the centerline (represented by the upper left corner) between the probes, and yet, it also spreads out as deep as 5 mm before the current density drops to half of the high values. This fact, coupled with the relatively low bulk lifetime that we normally encounter for commercial PV-grade CZ-Si, implies that the surface effect is minimal. In addition, the influence is confined to the top surface, which is much less than that of a double-sided wafer of similar thickness.





Fig. 3b Electric-potential contours on the center vertical plane containing the probes. conduction 18: F



Fig. 4 Current-density distribution at the center vertical plane between the probes.

Fig. 5 shows the calculated PCD signal with a surface recombination velocity of 10^5 cm/s compared with a signal with no surface effect. The surface effect only lowers the effective lifetime to 18.8 µs from the actual bulk lifetime of 20 µs. Therefore, as long as the measured lifetime is relatively low (\sim 50 µs), the effective lifetime may be safely assumed to be the bulk lifetime, within measurement error.



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Fig. 5 Simulated PCD signal for surface effects (horizontal time scale in seconds).

In actual measurements, the initial segment of the signal should be ignored because of the high-order surface effect. The tail segment may also be ingnored because of complications arising from carrier drifting by the electric field to the contact area and possible trapping effects.

MEASUREMENTS

Initial results are reported here to demonstrate repeatability of the instrument and its effectiveness in distinguishing CZ-Si ingots grown from different feedstock sources.

Repeatability

A repeatability study in the form of balanced Analysis of Variances (ANOVA) [3] has shown that the instrument is capable of detecting the difference between a) the front and back of the ingots, and b) the locations on the same end of the ingot. In both cases, repeated measurement at the same location is not a significant source of variation, as Table 1 shows. The P-value is the probability that the source factor is not significant.

Table 1. Repeatability Results of ANOVA

a)	Analysis of	f Var	iance for Li	fetime. Ing	ot fron	t and b	ack.
	Source	DF	SS	MS	F	P	
	Ingot	8	10303.80	1287.97	22.24	0.000	
	Frt/Bck	1	1579.34	1579.34	27.27	0.000	
	ReptUp/Down	n 2	25.80	12.90	0.22	0.801	
	ReptOnly	1	15.56	15.56	0.27	0.605	
	Error	95	5501.38	57.91			
	Total	107	17425.88				
b)	Analysis of	f Var	iance for Li	Eetime. Loo	ation.		
	Source	DF	SS	MS	F	P	
	Location	3	470.792	156.931	23.67	0.000	
	ReptUp/Down	n 2	6.750	3.375	0.51	0.610	
	ReptOnly	1	3.375	3.375	0.51	0.485	
	Error	17	112.708	6.630			
	total	23	593.625				

Separation by feedstock material

Over four hundred ingots grown from remelt, virgin, and pot-scrap material blends have been measured by the lifetime tester. Remelt is scrap material from CZ ingots including tops and tails, whereas pot-scrap is residual materal left in the crucible from previous growth runs. Fig. 6 presents the lifetime values in different categories of source material. It is clearly seen that pot-scrap had lower lifetimes than remelt or virgin material.



Fig. 6 Box plot for CZ-Si lifetimes vs. source material. Shown are median and quartile data, as well as highest and lowest values.

Separation by growth sequence

Ingots occasionally lose their dislocation-free single-crystal structure during Czochralski growth. At that point, the ingot (A) is tailed and pulled from the melt and another ingot (B) is grown from the remaining silicon charge. Lifetimes of the tail ends of both A and B crystals are measured and are shown in Fig. 7. Note that A crystals always have dislocations at the tail ends, and yet, lifetimes of B crystals are usually lower. This indicates that the higher impurity content because of impurity segregation effect in B crystals has masked the possible effect of dislocations in A crystals.



Fig. 7 Lifetime of tails of first (A) and second (B) run ingots.

SUMMARY

We have developed a lifetime instrument to characterize silicon ingots before they are subjected to expensive slicing and solar-cell processing. A 3-dimensional finite-element analysis indicates that the as-cut surface finish is adequate for measuring the bulk lifetime on the order of 50 μ s or less. Measurement repeatability and clear distinction among different grades of feedstock materials have been demonstrated.

Oxygen thermal-donor effects could be present in all the measurement results. To establish a relationship between ingot lifetime and final solar-cell performance (assuming

no significant contamination down the processing line), thermal-donor annealing may be necessary before more reliable lifetime measurements can be made.

ACKNOWLEDGEMENTS

This project is supported by a funds-in CRADA between Siemens Solar Industries and the National Renewable Energy Laboratory and by the U.S. Department of Energy under Contract No. DE-AC36-83CH10093 to the National Renewable Energy Laboratory.

The authors thank D. Sheadel, A. Smith, J. Coleman, J. Palm, and T. Jester of Siemens Solar Industries for technical assistance and support.

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