

Record efficiency multijunction solar cells with strain-balanced quantum well superlattices

Outline:

- Overview of quantum wells
- Fabrication and characterization
- GaInP top cells
- Triple junction results

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Analysis: John Geisz Growth: Waldo Olavarria, Alan Kibbler Processing and characterization: Michelle Young Top cell development: Manuel Hinojosa (IES-UPM) TEM: Jenny Selvidge (UCSB) Official Measurements: Tao Song





What is needed for cost-effective, high efficiency solar cells?

High efficiency architectures

- Absorb as many photons as possible
- Minimize voltage losses
- Spectral insensitivity?

Low-cost growth and fabrication

- Inexpensive source material
- High throughput
- Good source utilization

Low-cost substrates

- Remove and reuse the substrate
- Grow on something very inexpensive



Bandgaps for a three-junction cell





Bandgaps for a three-junction cell

GaAs is slightly too high a bandgap for AM1.5g (and AM0)



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L. GalnAsN

- 2. Metamorphic epitaxy
- 3. Quantum wells

Material would be lattice-matched

GaInAsN tends to have a short diffusion length, leading to a poor quantum efficiency







Triple junction cell architecture



Energy levels in a quantum well

"Band edge"

= Raw bandgap in the well (1.27 eV for $Ga_{0.894}In_{0.106}As$) + Effects of strain (\rightarrow 1.31 eV) + Effects of 2D quantum confinement (\rightarrow 1.35 eV) +

Effects of well asymmetry due to voltage bias (quantum confined Stark effect \rightarrow very small effect)



(schematic, not drawn to scale)

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Transport in quantum wells



GaAs-QW state of the art (c. 2020)



How can we maximize the absorption in the quantum wells?

 \rightarrow Thin the barriers, so that a larger fraction of each QW is made of absorbing well material.



in situ stress – thick barriers



Red and blue data show orthogonal [110] directions

in situ stress – thin barriers



Effect of AsH₃ flow in the GaInAs well material



EDS Comparison Group III

Low AsH₃

High AsH₃



TEM images from Jenny Selvidge, UCSB NREL | 19

EDS Comparison Group V

Low AsH₃

High AsH₃



TEM images from Jenny Selvidge, UCSB NREL | 20

Limit elastic relaxation by increasing AsH₃ flow

Increased AsH₃ flow during InGaAs QWs limits indium surface mobility



Barriers: 36 Å, $GaAs_{0.4}P_{0.6}$ Number of wells = 168

Vary the barrier thickness and the number of wells



Vary the number of wells and the depletion layer thickness

Variable i-region thickness



Barriers: 60 Å, GaAs_{0.65}P_{0.35}

Clear increase in sub-bandgap absorption and collection



Gives easy access to the device backside for applying advanced contacts Enables a range of device designs



Annealing of front-junction GaInP



Hypothesis: point defect passivation



Hypothesis: point defect passivation



X = Zn interstitial, Y = Ga vacancy

Hypothesis: point defect passivation



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X = Zn interstitial, Y = Ga vacancy
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Subcell analysis: AM1.5G and AM0



Woc = E_g/q - Voc : GalnP = 0.41 V / GaAs-QW = 0.35 V / LMM GalnAs = 0.35 V

Record three-Junction GaInP / GaAs+MQW / GaInAs cells



184 QWs, ~1560 nm of GalnAs No DBR behind the QWs

France et al., Joule, to appear May 18



New world record!

Record three-Junction GaInP / GaAs+MQW / GaInAs cells

GaInP/mqw-GaAs/GaInAs Cell Device ID: MT845An4 Device temperature: 24.2 ± 0.2 °C Device area: $0.242 \text{ cm}^2 \pm 0.1\%$ 4:38 PM 9/23/2021 Irradiance: 1000.0 W/m² Spectrum: ASTM G173 global OSMSS IV System ∷NREL PV Cell & Module Performance Α 40 **MT845** this work Current Density (mA/cm² 38 Spectrum AM1.5g AM0 X Irr. (W/m²) 1000 1366 -5 G Efficiency (%) Temp. (°C) 25 progression in record 28 66 36 efficiency by adding junctions Jsc (mA/cm²) 15.4 18.2 -10 34 Voc (V) 3.00 3.05 Cell Type Year FF (%) 85.3 83.8 LM 2018 Eff (%) 39.5 34.2 LMQW 2021 32 -15 IMM 2014 IMM 2016 WB 2014 30 IMM 2020 -20 0.0 1.0 2.0 3.0 2 6 3 5 4 Voltage (V) Number of junctions

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Thank you

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