



SO₃ CLEANING PROCESS IN SEMICONDUCTOR MANUFACTURING

BENEFITS

- Significant energy savings compared to “dry” stripping with high-energy oxygen-ion plasmas are expected
- Reduced radiation and charging damage and reduced damaged product waste
- Improved product quality
- Reduced hazardous chemical use compared to “wet” stripping with acid solutions or organic solvents
- More efficient removal of photoresist at less cost

APPLICATIONS

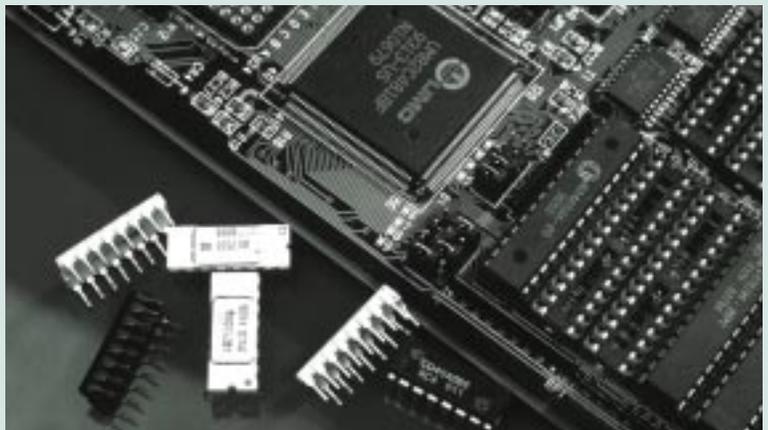
This technology is applicable to the semiconductor industry that produces semiconducting wafers for computers and other electronic equipment.

CLEANING SEMICONDUCTORS WITH SULFUR TRIOXIDE GAS REMOVES PHOTORESIST WITH LESS HAZARDOUS CHEMICALS

Removing photoresist, a light-sensitive material used in the manufacture of semiconductors, is fast becoming a major concern for United States manufacturers of semiconductors bound for computers and other electronic equipment. The hardened photoresist must be thoroughly cleaned from very small crevices on the semiconductor wafer at various stages in the manufacturing process.

A new sulfur trioxide (SO₃) gas-phase cleaning process developed by Anon, Inc., removes residual photoresist and organic polymers through a simple, two-step exposure to SO₃ followed by a deionized water rinse. Anon’s technology is technically and economically superior to existing semiconductor cleaning methods. By eliminating the use of damaging high-energy plasmas used in dry, semiconductor cleaning methods, the Anon process improves semiconductor yield and reliability. By eliminating costly wet chemical cleans, the Anon process eliminates the use of large volumes of hazardous or toxic chemicals. These improvements in the effectiveness and efficiency of photoresist cleaning translate into major improvements in manufacturing capability and dramatic reductions in cleaning costs for manufacturers of semiconductors.

SEMICONDUCTOR WAFER PRODUCTION



Photoresist, a light-sensitive material, is used to produce semiconductor wafers for computers. Removing hardened photoresist can now be achieved without damage and with reduced use of hazardous chemicals by exposing the wafers to a gas process developed by Anon, Inc.



Project Description

Goal: The goal of this project is to demonstrate that photoresist can be effectively cleaned from semiconductor wafers in processing by exposing them to a SO_3 gas, followed by a rinsing in deionized water. This process is anticipated to substantially replace damaging dry stripping and hazardous-waste-producing wet stripping in the semiconductor manufacturing industry.

Progress and Milestones

- The process is being tested on substrates from several major semiconductor and flat-panel display manufacturers, with impressive results.
- A computer-controlled photoresist stripping tool was built to implement the process. It was demonstrated successfully on a variety of semiconductor cleaning applications.
- Anon has built a small clean room on-site to simulate clean conditions for demonstrating the process.



NICE³—National Industrial Competitive-ness through Energy, Environment and Economics: An innovative, cost-sharing program to promote energy efficiency, clean production, and economic competitiveness in industry. This grant program provides funding to state and industry partnerships for projects that demonstrate advances in energy efficiency and clean production technologies. Total project cost for a single award must be cost-shared at a minimum of 50% by a combination of state and industrial partner dollars. The DOE share for each award shall not exceed \$400,000 to the industrial partner and up to \$25,000 to the sponsoring state agency for a maximum of \$425,000. Each award may cover a project period of up to three years.

PROJECT PARTNERS

NICE³ Program
U.S. Department of Energy
Washington, DC

Anon, Inc.
San Jose, CA

California Energy Commission
Sacramento, CA

FOR PROJECT INFORMATION, CONTACT:

Eric Levenson
Anon, Inc.
621 B River Oaks Parkway
San Jose, CA 95134-1907
Phone: (408) 428-2860
Fax: (408) 434-0138

FOR PROGRAM INFORMATION, CONTACT:

Lisa Barnett
Program Manager, NICE³ Program
U.S. Department of Energy
1000 Independence Ave., SW
Washington, DC 20585
Phone: (202) 586-2212
Fax: (202) 586-7114
lisa.barnett@ee.doe.gov

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Office of Industrial Technologies
Energy Efficiency
and Renewable Energy
U.S. Department of Energy
Washington, DC 20585



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